CURRICULUM VITAE



NAME : ANURAG TRIPATHI

DATE OF BIRTH : 27-11-1971

NATIONALITY : INDIAN

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EDUCATIONAL OUALIFUCATIONS:

Sl.No.	Name of the Class/Course	Name of the Institute/Univ.	Year of Passing	Percentage	Remarks
1.	High School Exam	Christ Jyoti School Rewa, M.P. (M.P.Board)	1987	79.5 %	Stood 57th in the state [M.P.]
2.	Higher Secondary Exam	Do 1989		83.2 %	Stood 29th in the state [M.P.]
3.	B.Tech Electrical Engg	Govt. Engg. College Rewa M.P. (Under A.P.S. Univ. Rewa)	1993	81.2 %	Gold Medalist in Electrical Engg Branch in the university
4.	M. Tech. (Control & Instrumentation)	MNNIT, Allahabad	2002	69 %	
5.	Ph.D. (Power Electronics & Drives)	UPTU, Lucknow	Started in Feb.2006	Awarded in September 2010	

PUBLICATIONS:

- 1) Two books published with Wiley (plus One in Printing Stage)
- 2) One paper in IEEE magazine,
- 3) Six papers published in IEEE International Conferences.
- 4) **Over FIFTY** papers published in International Journals.
- 5) Four papers in National Conferences.
- 6) **Two** papers in National Journals.

WORK EXPERIENCE:

Sl.No.	Employer	Place/Nature of Job	Period/Duration	Remarks	
1.	Govt. Engg. College Rewa (M.P.)	Guest Lecturer in Electrical Engg. Department	July 1994 to August 1995 1 year & 2 months	Taught Electrical Networks, Basic Electrical Engg, Control Systems & Power Electronics	
2.	Steel Authority of India Ltd.	Assistant Manager in a hot rolling mill in Durgapur Steel Plant West Bengal	September 1995 to March 2000 4 years & 6 Months	Worked as a frontline Executive in shifts and looked after the Electrical Maintenance of the Mill.	
3.	Institute of Engg. & Technology, Sitapur Road, Lucknow.	Guest Lecturer in Electrical Engg. Department	April 2000 – August 2002 2 years & 5 months	Taught Electrical Networks, Basic Electrical Engg, Control Systems & Power Electronics Conducted Labs of the above subjects. Guided project works of the final year students	
4.	Institute of Engg. & Technology, Sitapur Road, Lucknow.	Assistant Professor (initially termed as Lecturer) inElectrical Engg. Department	17 th August 2002- 2015 13 years	Taught Electrical Networks, Basic Electrical Engg, Control Systems & Power Electronics Conducted Labs of the above subjects. Guided project works of the final year students	
5.	Institute of Engg. & Technology, Sitapur Road, Lucknow.	Associate Professor in Electrical Engg. Department	17 th August 2015- 17 th August 2018. I worked in IET till 2 nd October 2017 2 years and 1month	Taught Electrical Networks, Basic Electrical Engg, Control Systems & Power Electronics Conducted Labs of the above subjects. Guided project works of the final year students *** I GOT PROMOTED UNDER CAS (CAREER ADVANCEMENT SCHEME) AT THE POST OF PROFESSOR FROM 17 TH AUGUST 2018	
6.	Dr. APJ Abdul Kalam Technical University, Lucknow	Additional Controller of Examination and subsequently Controller of Examination (On Deputation)	3 rd October 2017 to 22 nd December 2022 5 years and two months	All Examination Related work concerning around 763 colleges of AKTU	
7.	Institute of Engg. & Technology, Sitapur Road, Lucknow.	Professor inElectrical Engg. Department	23 rd December 2022 to Present day Six months	Teaching Electrical Networks, Basic Electrical Engg, Control Systems & Power Electronics Conducted Labs of the above subjects. Guided project works of the final year students	
TOTAL WORK EXPERIENCE:			28 YEARS TEN MONTHS out of which 25 years & 3 months as regular Govt.employee and remaining 3 years & 7 months as guest Lecturer		

Remarks:

- 1. Taught M.Tech courses [along with Modular M.Tech] in Electrical Engg. at IET Lucknow, MSc Electronics courses in Lucknow Univ., and have guided Twelve M.Tech dessertations.
- 2. Supervised Eleven PhDs (degrees awarded), one permitted to submit.
- 3. Presently supervising Seven PhDs.

[Dr. Anurag Tripathi]