DR. SUBODH WAIRYA

PROFESSOR

Department of Electronic & Communication Engineering

(NBA Accredited Till 2025)

Institute of Engineering & Technology, Lucknow

(An Autonomous Constitute Institute of Dr. A. P. J. Abdul Kalam Technical University, Lucknow, India)

1. **FATHER'S NAME** : Shri Prem Prakash Wairya

2. **ADDRESS FOR CORRESPONDENCE** : Type –IV B 302 Block (3), Institute of Engineering and Technology Campus, Sitapur Road, Lucknow- 226021, India.

3. **DATE OF BIRTH** : **17th March, 1970**

4. **EMAIL** : **swairya@gmail.com, subodh.wariya@ietlucknow.ac.in**

5. **CONTACT NO** : **+91 9044039593, +91 9415159085**

6. **PAN No./HIGH SCHOOL Certificate:** AADPW3496R/0404752

7. EDUCATIONAL QUALIFICATIONS:

S.N	QUALIFICATIONS	SUBJECT	YEAR	DIV	% of Marks	COLLEGE/UNIV
1.	Ph.D	Electronics Engineering (**Quality Improvement Program**)	2012		9.5 CPI	MNNIT, Allahabad, Uttar Pradesh, India
2.	M. E.	Tele-Communication Engineering (**Quality Improvement Program**)	2002	1st	85%	Jadavpur University, Kolkata, India
3.	B. TECH.	Electronics Engineering	1993	1st	73.1%	HBTI, Kanpur, Uttar Pradesh, India
4.	INTERMEDIATE	Hindi, English, Mathematics, Physics, Chemistry	1988	1st	70.1%	U.P.BOARD
5.	HIGH SCHOOL	Hindi, English, Math Science, Social Science, Biology,	1986	1st	70.2%	U.P.BOARD
	PUBLICATIONS	Journals International Conference ❖ IEEE Xplore Digital Library ❖ Book Chapter (Lecture Note) National Conference/Workshop Book	68 52 12 25 12 03		55 SCI/ SCOPUS	**Total 140**
	PhD Registered M.Tech (Thesis)	**02 (Awarded) 9 (in Process)** **39 (Guided) 03 (in Process)**				**11** **42**

WORKS EXPERIENCE: 29 years

Teaching: (27 Years) Institute of Engineering and Technology (I.E.T), Lucknow, U.P**.**

Designation	: **PROFESSOR**
Period	: From **12th Dec 2012**- till Date
Pay Scale	**Basic Pay 1,88,400=00** (1st July 2022) 7th Pay Band (14 Level) Pay Band **(Rs.144200-218200) 7th CPC AGP- 10000/-** Pay Band (37,400- 67,000) 6th CPC Grade Pay 10000)
Designation	: **ASSOCIATE PROFESSOR**
Period	: From 6th May 2009 to 11th Dec 2012
Pay scale	: 37,400- 67,000 Grade Pay 9000)
Designation	: **ASSISTANT PROFESSOR**
Period	: From 6th May 2006- 5thMay 2009
Pay scale	15600 - 39100 Grade Pay 8000)
Designation	: **ASSISTANT PROFESSOR** (Lecturer /Sr. Lecture)
Period	: From 5th May 1996- 5thMay 2006

(b**) Research &Industry: 2 years 6 months**

Defence Research & Development Organization (DRDO), Lucknow (One Year)

Period	: From January, 1995-January, 1996
Designation	: Scientist "B" Adhoc (One Year)
Responsibilities	: Certification and Design Projects

Hindustan Aeronautical Limited, Lucknow (One Year)

Period	: From January, 1994-January, 1995 (one year)
Designation	: Graduate Engineer under Consultancy Project
Responsibilities	: Design Project

Govt. Polytechnics, Lucknow (Six Months)

Period	: From August 1993-January, 1994 (6 Month)
Designation	: Guest Faculty
Responsibilities	: Teaching

Research:

- **Ph.D on (Thesis)** "**Performance Evaluation of High Speed Low Power CMOS Full Adder Circuits For Low Voltage VLSI Design** from Motilal Nehru National Institute of Technology (MNNIT), Allahabad, U.P., India.
- **M.Tech Dissertation on (Thesis)** "**Some Study on Polarization Properties In Single-Mode Fiber and Passive Component**" from Jadavpur University, Kolkata, India, 700032.

Books:

1. **A Simplified Approach to Telecommunication and Electronic Switching Systems,** C.B.L. Srivastav, Neelam Srivastava & Subodh Kumar Wairya, Published by Dhanpat Rai and Company**.**
2. **Design and Testability of Diverse Reversible Error Control Circuits**, Neeraj Kumar Misra, Subodh Wairya, Bibhash Sen, LAP Lambert Academic Publishing German, August 2017, Pages 107, DOI: 978-620-2-01508-0.
3. **Intelligent Systems and Smart Infrastructure** Proceedings of ICISSI 2022, Edited By Brijesh Mishra, Rakesh Kumar Singh, Subodh Wairya, Manish Tiwari, **Pages 774, 2023, CRC Press, Taylor &Francis Group, ISBN 9781032412870.**

PARTICIPATION IN

ACADAMIC ACTIVITIES

INSTITUTE ACTIVITES

DEPARTMENTAL ACTIVITES

TECHNICAL UNIVERSITY ACTIVITIES

ACADAMIC ACTIVITIES:

(a) SHORT TERM COURSES:

1. Workshop on "**Active Learning, Autonomy, Academic Governance and R & D**", July 02-06, organized by IIT Roorkee.
2. TEQUIP II Sponsored Faculty Development Programme (FDP) on "Internet of Things" Conducted by ESCI, The Institution of Engineers (India) Engineering Staff College of India (ESCI) Hyderabad, held on 06- 10th March 2017 (One Weeks).
3. AICTE Sponsored Short Term Course on "Cyber Crime & Forensic Tools Through ICT" ,**Jan. 27th– 31st Jan 2014 (One Week)** organized by the Department of Computer Science, NITTTR Chandigarh, India.
4. Faculty Development Training Programme Entitled "Cadence Tool Training Course for India-Chip 2010 Tapeout", organized by Department of Electrical Engineering IIT Kanpur, and held on **29th June to 4th July 2009 (One Week).**
5. AICTE(MHRD) Sponsored Faculty Development Programme on "Issues & Design of Distributed system and its Application" **Jan.27th–Feb. 7th 2009, (Two Week)** organized by the Department of Computer Science and Engineering, Motilal Nehru National Institute of Technology (MNNIT), Allahabad, India
6. AICTE(MHRD) Sponsored Faculty Development Programme on "VLSI for Signal Processing & Communication" **January 12–24, 2009 (Two Week)** organized by the Department of Electronics and Communication Engineering, Motilal Nehru National Institute of Technology (MNNIT), Allahabad, India
7. Short Term Course on "Wireless Networks" **May 23-28, 2005, (One week)** conducted by G.S. Sanyal School of Telecommunication, IIT, Kharagpur.
8. AICTE-ISTE Short Term Course on "Recent Advance in Power Semiconductor Devices and Their Application" **July 01-12, 2002, (Two Week)** organized by the Department of Electrical Engineering, Delhi College of Engineering , Delhi
9. U.G.C. Sponsored refresher Course on "Computer Aided Design of VLSI Circuits" **March 07-27, 2001, (Three Week)** organized by the Department of Electronics &Telecommunication Engineering, Jadavpur University, Kolkata, India

Workshop/Seminar/Conferences as Resource Person

S. No.	Topic of Course	Place	Date
1.	National Conference on "Emerging Technology and Trends in IT 2007 (NCET 2007)"	ITS, Ghaziabad	December 06th -07th , 2007
2.	National Seminar on "Nanostructures and Devices"	Invertis Institute of Engineering and Technology, Bareilly	April 7th -8th , 2007
3.	Workshop titled "VLSI Design Tools"	Department of Electronics Engineering, Institute of Engineering and Technology, Lucknow	November 17th -18th , 2007
4.	National Seminar on "Recent Trends on Digital Communication"	Invertis Institute of Engineering and Technology, Bareilly	February 2nd -3th , 2008
5.	Workshop titled "Workshop on Practical on MATLAB for B.Tech Courses	Department of Electronics Engineering, Institute of Engineering and Technology, Lucknow	April 8th -10th , 2008
6.	Workshop titled "Practical Workshop on Embedded System Designs Based on Micro controllers"	Department of Electronics Engineering, Institute of Engineering and Technology, Lucknow	February 28th -29th , 2008
7.	Workshop on "Curricula Review for B.Tech Electronics and Communication Engineering"	Department of Electronics, Institute of Engineering and Technology, UPTU,	June 15th -16th, 2015

WORKSHOP

1. Workshop on Hardware Implementation of Embedded System Design, IoT Development and DSP Application (HIEID-2019), 19th – 20thSeptember, 2019, EC Deptt IET, Lucknow.
2. Workshop ON Microcontroller Based System Design, 20th September, 2019, EC Deptt IET, Lucknow.
3. TEQUIP III sponsored Workshop on "Renewable Energy and Environment" conducted by Department of Chemical Engineering, Institute of Engineering & Technology Lucknow, Uttar Pradesh on 27th February,2018.
4. Workshop on "Advances in Chemical Engineering and Technology" conducted by Department of Chemical Engineering, Institute of Engineering & Technology Lucknow, Uttar Pradesh on 23rd-24th March,2018.
5. TEQUIP II Sponsored Faculty Development Programme (FDP) on "Pedagogy and Management Capacity Enhancement Program For Teaching Staff" Conducted by ESCI, The Institution of Engineers (India) Engineering Staff College of India (ESCI) Hyderabad, held on 17-19th March 2017
6. Attended NBA Oriented Workshop on Outcome Based Education and Accreditation for Programme Evaluators (PEVs), organized by IIT Kanpur, Uttar Pradesh, India 1st Oct 2016.
7. **Attended NBA Workshop on "SAR Filling through Active Instructional Methods"** organized by Engineering Staff College of India (ESCI) Hyderabad in Collaboration with Institute of Engineering & Technology, Lucknow, held on 12-13 August 2016
8. **Attended National Workshop on "Outcome based Education & NBA Accreditation"** organized by State Project Facilitation Unit (SPFU) Uttar Pradesh in Collaboration with Engineering Staff College of India (ESCI) Hyderabad, held on 01-03 July 2016
9. Attended Workshop on "Curricula Review for B.Tech Electronics and Communication Engineering" organized by Department of Electronics, Institute of Engineering and Technology, UPTU, Under Technical Education Quality Improvement Program (TEQIP), held on 15th -16th June, 2015
10. Attended Workshop on "Industry Academia Interaction on Frugal Engineering" organized by Institute of Engineering and Technology, Gautama Buddha Technical University, Lucknow, held on 26th October, 2013.
11. Attended Workshop on "E-WASTE MANAGEMENT- CHALLENGES, PROSPECTES AND STRATEGIES" organized by PHD Chamber of Commerce and Industry in association with Ministry of Environment and Forests, Government of India, held on 26th September, 2013.
12. Attended Workshop on "Industry Academia Interaction for Innovation and Quality Technical Education: A Path Forward for 2020" organized by Institute of Engineering and Technology, Gautama Buddha Technical University, Lucknow, held on 04th September, 2013.
13. Attended Workshop on "REAL TIME SIMULATION" organized by Department Of Electrical Engineering, I.E.T., Lucknow, India held on 26th September, 2011.
14. Attended Workshop on "Virtual Instrumentation & Its Applications (WVAI 09)" organized by Department of Electrical Engineering, MNNIT Allahabad, India held on 18th -20th March ,2009
15. Organized & attended a Workshop titled "Workshop on Practical on MATLAB for B.Tech Courses organized by Department of Electronics Engineering, IET Lucknow, India held on 8-10th April, 2008.
16. Organized & attended a Workshop titled "Practical Workshop on Embedded System Designs Based on Micro controllers" organized by Department of Electronics Engineering, IET Lucknow, India held on February 28-29th, 2008.
17. Organized & attended a Workshop titled "VLSI Design Tools" organized by the Department of Electronics Engineering, Institute of Engineering and Technology, Lucknow, India on 17-18 Nov. 2007.
18. Attended Workshop on "Adhoc and Sensor Networks" organized by IIIT, Allahabad held on 17th -19th December, 2006.

Innovation Startup and Entrepreneurship work performed:

- **Innovation Gallery**: Innovation gallery is unique program under which University students are asked to share innovative idea on specific theme. In Jan 2020, students presented their idea on "Innovation on Automobile" in which six ideas are selected for prototype development.
- **Innovation Gallery:** Innovative ideas were invited under the UP government scheme One District One Project, in which 157 ideas were received from students. After screening 14 ideas were shortlisted for prototype development.
- **DST – NIMAT Programs:** Entrepreneurship Institute of India (EDII), Ahemdabad sanctioned two Entrepreneurship Awareness Camp (EAC) programs to University. These programs were successfully organized in September – October 2019 and Post Project Report (PPR) and UC submitted to EDII. These programs were sponsored by Department of Science and Technology (DST), Government of India.
- **Organised Aatmnirbhar Bharat Abhiyaan e-lecture series:** To aware and strengthen students for entrepreneurship University started Aatmnirbhar Bharat Abhiyaan e-lecture series in the time of covid – 19 pandemic. The motive of this series was to connect students with Vocal for Global initiative.

Details of e-lectures

Sn	Date	Title of lecture	Name of Speaker
1	13 Aug. 2020	Startup Opportunities in Atmanirbhar Bharat Abhiyan	Dr. Amit Kr. Dwivedi, Associate Prof. EDII Ahmedabad
2	18 Aug 2020	Role of IP Protection in Atmanirbhar Bharat Abhiyan	Shri Ravi Pandey, Research Establishment Officer, IIT Kanpur
3	20 Aug 2020	Indian Startup Ecosystem for Atmanirbhar Bharat Abhiyan	Dr. SatyaRanjanAcharya, , Associate Prof. EDII Ahmedabad
4	28 Aug 2020	Role Manufacturing Sector in Atmanirbhar Bharat Abhiyan	Shri Ravi S Kochak, Vice Chairman, IMechE, South Ashia Region
5	2 Sept 2020	Role of Fintech in Atmanirbhar Bharat Abhiyan	Prof Prakash Singh, Prof IIM Lucknow
6	09 Sept 2020	Steps for building institutional Innovation Ecosystem	Dr. Sanjay P Chauhan Director GTU Innovation Council
7	12 Sept 2020	Steps towards entrepreneurial Ecosystem	Shri Tushar K.Pranchal Incubation Manger, GTU Innovation Cell
8	02 Sept 2020	Identifying Problem Statement and Opportunities	Shri ShailendraJaiswal Principal Executive Director, RDSO, New Delhi
9	10 Oct 2020	How to be ready for future	Dr. Kamal Karnatak , Group CIO, RJ Corp
10	14 Oct 2020	Scientific Research: Metrics and Visibility	Dr. Vivek Kumar Singh Professor CS BHU Varanasi
11	21 Oct 2020	Basics of financial Literacy	Ms.Neha Mishra, CEO The FinLit Project, Lucknow
12	03 Dec 2020	Entrepreneurship: Business creation and Development	Shri SujitBanarjee, Director DST, Delhi
13	6 Jan 2021	Transform your Startup with Data Driven Insights	Shri Bohitesh Mishra
14	27 Jan 2021	Basics of Data Science and Analytics (DSA) using R (set of four lectures)	Shri Bohitesh Mishra
15	23 Jan 2021	Telephonic Interview Etiquette	Shri Dinesh Pathak
16	30 Jan 2021	Video Interview	Shri Dinesh Pathak

- **Reviewer (**Technical Program Committee member) **for some reputes (**International Journals): The Journal of Supercomputing, Journal of Computing and Digital Systems, Electronics Letter, Journal of Engineering Research and Reports, Int. J. of Circuits and Architecture Design (Inderscience Publishers Ltd). etc

- **Reviewer and Program Committee Member of International Conferences**

S. No.	Title of Conference	Place	Date
1.	International Conference on Signal Processing & Integrated Networks (SPIN) (Technical Program Committee member)	AMITY University Noida, U.P. India	2023-2014
2.	8th INTERNATIONAL CONFERENCE ON SIGNAL PROCESSING AND COMMUNICATION (ICSC 2022)	JIIT Noida, U.P. India	1-3 Dec 2022
3.	5th International Conference on Multimedia, Signal Processing and Communication Technologies (IMPACT 2022)	AMU, Aligarh Musilam University, Aligarh, U.P. India	26-27 Nov. 2022
4.	AICTE Sponsored 2nd International conference on Advancement in Electronics & Communication Engineering (AECE-2022)Program Committee member	Raj Kumar Goel Institute of Technology (RKGIT) Ghaziabad, Uttar Pradesh, India	14th-15thJuly 2022
5.	International Conference on Intelligent Systems and Smart Infrastructure (ICISSI 2022) Program Committee member	Shambhunath Institute of Engineering and Technology, Prayagraj UP India,	21-22 May, 2022
6.	International Conference on VLSI, Communication and Signal Processing (VCAS) (Technical Program Committee member)	MNNIT, Prayagraj, U.P. India	2022-2019
7.	International Conference On VLSI & Microwave and Wireless Technologies (ICVMWT-2021), MMMTU, Gorakhpur, India, 20-21 March. 2021 (subreviewer) https://login.easychair.org/conferences/review_requests?a=25970358	MMMTU, Gorakhpur, India	20-21 March. 2021
8.	Organized (Coordinator) International Conference on "Defence and Space Technologies (ICDST) 2019 by Department of Electronics & Communication Engineering, Institute of Engineering & Technology, Lucknow, 23-25 August 2019. https://www.ietlucknow.ac.in/sites/default/files/mag/conference%20-%20icdst%202019%20leaflet.pdf	Institute of Engineering & Technology, Lucknow, UP, India	23-25 August 2019
9.	International Conference on VLSI, Communication and Signal Processing (VCAS 2019) (subreviewer) https://login.easychair.org/conferences/review_requests?a=23038920	MNNIT Allahabad, India	2021 - 2019
10.	International Conference on "Defence and Space Technologies (ICDST) 2019 ICDST 2019 (subreviewer) https://login.easychair.org/conferences/review_requests?a=22948676	Institute of Engineering & Technology, Lucknow, UP, India	23-25 August 2019.
11.	International Conference- Confluence 2013: The Next Generation Information Technology Summit (Confluence 2014)	AMITY University Noida	2016-2014
12.	International Conference on Medical Imaging, m-Health and Emerging Communication Systems (MedCom-2014)	GL Bajaj Institute Gr. Noida	Nov. 7-8 2014
13.	1st International Conference on Next Generation Computing Technologies (NGCT 2015)	University of Petroleum & Energy Studies, Dehradun	Sept 4-5 2015

PARTICIPATION IN **CONFERENCES/SEMINAR**

1. **Best Paper Award**, Paper title "Performance analysis of various fast and low power dynamic comparators", International Conference on Intelligent Systems and Smart Infrastructure (ICISSI 2022) India, 21-22 May, 2022 at Shambhunath Institute of Engineering and Technology, Prayagraj UP India.
2. General-chair, International Conference on Intelligent Systems and Smart Infrastructure (ICISSI 2022) Organized by Shambhunath Institute of Engineering and Technology, Prayagraj UP India, Institute of Engineering and Technology (IET) Lucknow, U.P India, and Manipal University Jaipur, Rajasthan India, CRC Press, 21-22 May, 2022
3. Conference Co-chair, First International Conference on Advances in Computing and Future Communication Technologies ICACFCT-2021 Organized by Meerut Institute of Engineering & Technology and ACIC MIET, 16-17 December, 2021.
4. Advisory Committee member in International Conference on Modern Approaches in Engineering, Science and Management (MAESM-2021) Organized by Bansal Institute of Engineering and Technology, Lucknow, India, 16-17 April 2021.
5. **Best Paper Award**, Paper title "A Cost efficient QCA RAM cell for Nanotechnology Applications", International Conference On VLSI & Microwave and Wireless Technologies (ICVMWT-2021), 20-21 March. **2021 at** MMMTU, Gorakhpur, India.
6. **Presented a paper**, "An Efficient QCA Vedic Multiplier for Nanotechnology applications", IEEE 2021 The International Conference for Intelligent Technologies, (CONIT 2021), The KLE Institute of Technology, Hubballi, Kartakata , India, 25-27 June, 2021
7. **Presented a paper** "Performance Improvement and Comparative Analysis of Memristive Emulator Networks", IEEE 2nd International Conference on Emergining Technologies (IEEE INCET2021), Belguam, Karnataka,, India, 21-23 May 2021..
8. **Organized (Coordinator)** International Conference on "Defense and Space Technologies (ICDST) 2019 by Department of Electronics & Communication Engineering, Institute of Engineering & Technology, Lucknow, 23-25 August 2019.
9. **Organised (Co-ordinator)** National Conference "Emerging Trends in Electrical & Electronics Engineering (NCETEEE'16), by Department of Electronics & Communication Engineering & Department of Electrical Engineering Institute of Engineering & Technology, Lucknow, 19-20 August, 2016.
10. Attended and Programme Technical Committee member in International Conference on "Signal Processing and Integrated Network (SPIN-2015)," organized by Department of Electronics Engineering, AMITY University, Uttar Pradesh, India Feb. 2015
11. Associate as Programme Technical Committee member in International Conference on "Medical Imaging m-Health & Emerging Areas in Communication Systems (MEDCOM- 2014), "organized by G.L. Bajaj Institute of Technology & Management, Gr. Noida, Uttar Pradesh, India proposed dates 7th-8th Nov. 2014
12. Associated as Programme Technical Committee member in International Conference on "Signal Processing and Integrated Network (SPIN-2014)," organized by Department of Electronics Engineering, AMITY University, Uttar Pradesh, India 20-21st Feb. 2014
13. Attended and Programme Technical Committee member, 4th International Conference on "The Next Generation Technology Summit (CONFLUENCE-2013)," organized by Department of Computer Science& Engineering, AMITY University, Uttar Pradesh, India 26-27th Sept. 2013.
14. Attended International Conference on Advance in Electrical & Electronics Engineering, (ICAEEE–2011), organized by Department of Electronics & Electrical Engineering, MIT, Moradabad, 25-26th Feb. 2011.
15. Attended 1stInternational Conference on "Power, Control & Embedded Systems (ICPCES 2010), organized by Department of Electrical Engineering, Motilal Nehru National Institute of Technology Allahabad, India, held on Nov. 29-Dec. 1, 2010.
16. Attended National Seminar on "Recent Trends on Digital Communication" organized by Invertis Institute of Engineering and Technology, Bareilly, held on February 2-3, 2008.

17. Attended International Conference on Ayurvedic Bhasm and Nanomedicine, organized by OMICS BIOTECHNOLOGY, USA,ACS-BIOINFORMATIC, Biotech Park, Lucknow, 22nd Dec. 2007.
18. National Conference on "Emerging Technology and Trends in IT 2007 (NCET 2007)" organized by ITS, Ghaziabad, held on 06-07 December, 2007.
19. National Seminar on "Nanostructures and Devices" organized by Invertis Institute of Engineering and Technology, Bareilly, held on April 7-8, 2007.
20. Attended 2nd International Conference on "Wireless Communication and Sensor Networks (WCSN)" organized by IIIT, Allahabad held on 17-19 December, 2006.
21. Attended International Conference on "Fiber Optics and Photonics" Advance Technology Center IIT Kharagpur, India and Department of Applied Physics University of Calcutta held on 18-20 December,2000

PARTICIPATION IN TRAINGING PROGRAM AND TUTORIALS:

1. Advisory Committee Member, Five day online Faculty Development Programme (FDP) Integration of Renewable Energy and Smart Grids for Smart Cities 07 -12, March 2022.. The FDP is organized by Department of Electrical Engineering, Bansal Institute of Engineering and Technology, Lucknow, U.P.
2. Advisory Committee Member, Five day online Faculty Development Programme (FDP) Data Analytics, Big Data, Machine Learning and Applications February 21 -26, Feb 2022. The FDP is organized by Department of Electrical Engineering, Bansal Institute of Engineering and Technology, Lucknow, U.P.
3. Invited Lecture talk on *"MEMS & Digital Design in VLSI Application*" in five day online Faculty Development Programme (FDP) on " **Recent Trends of Emerging Research Advances in Design Aspects and Innovative Modeling Techniques with Miniaturization for Electronics Devices and Circuits**" from 24-28 January, 2022. The FDP is sponsored by Online AICTE Training and Learning (ATAL) Academy Delhi and organized by Department of Electronics Engineering, IERT Prayagraj U.P.
4. Chairing a Technical Session on 24th September for 4th International Conference on VLSI, Communication & Signal Processing (VCAS-2021), ECE Department MNNIT, Allahabad, India, Sept. 24-26, 2021
5. Invited Lecture talk on "*Advance Digital Circuit Design in VLSI Application"* in five day online Faculty Development Programme on "**Current Research Trends in VLSI design and Device Modelling**" organized by Department of ECE, Atria Institute of Technology, Bengaluru, India in association with IEEE & IETE ATRIA Student Branch chapter (24th-28th August 2021) .
6. Chairing a Technical Session on 26th August for 8th International Conference on Signal Processing and Integrated Networks (SPIN 2021), organizing by Department of Electronics & Communication Engineering, Amity University, Noida, India in Technical Collaboration with IEEE, on 26-27 Aug 2021.
7. Invited Lecture talk in IETL online lecture series topic "Advance Digital circuit Design" organized by Department of EC, IET Lucknow and PEC Pondicherry for faculty under TEQIPIII twining activity on 13th Feb 2021.
8. Chairing a Technical Session for 3rd International Conference on VLSI, Communication & Signal Processing (VCAS 2020), MNNIT, Allahabad, October 9-11, 2020 organizing by ECE Deptt, MNNIT Allahabad, Prayagraj, Uttar Pradesh, India, on October 9-11, 2020.
9. Presented a paper "Proposing a Novel Low Power High-Speed Mixed GDI Full Adder Topology" 1st International Conference on Power, Control & Embedded Systems (ICPCES 2010), organized by Department of Electrical Engineering , Motilal Nehru National Institute of Technology (MNNIT), Allahabad, India held on Nov. 29-Dec. 1 2010.
10. Tutorial "MOSFET Scaling: trends, Challenges and Key Technology Innovations" organized by Department of Electrical Engineering, Motilal Nehru National Institute of Technology (MNNIT), Allahabad, India, held on Nov. 28, 2010.
11. Training Programme Entitled "ENERGY AUDITING" on March, 2006 under Technical Education Quality Improvement program at Institute of Engineering & Technology, Lucknow.
12. Workshop on "Hindi Sabdawali in Engineering Subject" organized by scientific & Technical Sabdawali Commission, MHRD Ministry, New Delhi, held on 19-20th March, 2005.

13. 88 Hours Course on "C,UNIX & DATA STRUCTURES" organized by Computer Science and Engineering Department, Jadavpur University, Calcutta, held on Sept 08-12 Dec, 2001.
14. Workshop on "Patent Awareness" organized by Patent Promotion and archival Cell, Jadavpur University, Calcutta, India held on 19th January, 2001.
15. Tutorial "PHOTONIC 2000" Organized by Advance Technology Center IIT Kharagpur, India and Department of Applied Physics University of Calcutta held on 17thDecember, 2000.

INSTITUTE ADMINISTRATIVE ACTIVITES:-

- Chief Warden May 2022-Till date
- Member Institute Library Advisory Committee (Till date)
- Dy. Dean Academics, IET, Lucknow (Jan. 2014-Dec 2016)
- Coordinator/Nodal Officer, AICTE Sponsored Employability Enhancement Training Program (EETP) organized by Bharat Sanchar Nigam Limited (BNSL). AICTE has entered into a MoU with BSNL to facilitate the Technical Institutions (June 2013-16).
- Member Examination Committee, IET Lucknow (2014- 2016)
- Co-coordinator, Financial Management TEQIP Phase II Committee, IET Lucknow (2012-2014).
- Member INSTECH Student Sports and Cultural council (ISSACC) IET, Lucknow (2011-2015)
- Faculty Incharge Sports & Culture Activity (ISSACC) I.E.T Lucknow (2002-2008)
- Warden, VA Hostel (July 2016- Sept 2017), VB Hostel (July 2012- Feb. 2014), (97-2000), New Boys Hostel (July 2011-June 2012), (2003-2007)
- Officer Incharge National Social Service (NSS) (1998-2000),
- Officer Incharge, National Cadet Corps (NCC) 67 UP Battalion ((2011-2013), 2004-2007)
- Deputy Registrar (officiating), I.E.T., Lucknow(1997-1998).
- Center Superintendent MTech/MPharma End Semester Exam (March 2014),
- Center Superintendent Special Carry Over End Semester Exam (Sep-Oct 2014)
- Assistant Center Superintendent, UPTU End Semester Exams IET Lucknow (2002-2004).
- Member Proctorial Board and Anti Ragging Committee IET, (2011-till date)

DEPARTMENTAL ACTIVITES

- Head of Department with effect from August 2016 to Oct 2019.
- Faculty Advisor Departmental Technical Society "Society for Electronics Exploration and Development" (SEED) EC Department, IET, Lucknow (2019-2020). https://www.ietlucknow.ac.in/sites/default/files/mag/Departmental%20Magazine%202019-20.pdf
- Coordinator M.Tech (Microelectronics) Program. (Jan. 2016 to Sept. 2017)
- Member of Board of Studies (BOS) EC Deptt IET Lucknow (2013-till date)
- Deputy Coordinator, M.Tech (Microelectronics) Program. (2011-2015)
- Member interview committee for the interview of Contractual faculty (2011-2014)
- Faculty Advisor, IInd Year EC/EI Students.
- Incharge Contractual Gust Faculty, EC Department, IET, Lucknow
- Faculty Advisor Departmental Technical Society "Society for Electronics Exploration and Development" (SEED) EC Department, IET, Lucknow (2011-2014)

  COURSE TAUGHT AT UNDER GRADUATE LEVEL (B.Tech) (1996-till date)
  - Basic Electronics
  - Electronics Devices and Circuits
  - Analog Integrated Circuits
  - Digital Logic Switching Theory
  - Digital Integrated Circuit
  - Signal and Systems
  - Digital Signal Processing
  - Principle of Communication
  - Digital Communication
  - Electronics Switching
  - VLSI Design

  UG LABORATORIES DEVELOPED/PERFORMED
  - Basic Electronics Lab
  - Printed circuit board (PCB) Lab
  - Microprocessor Lab
  - CAD Lab

  COURSE TAUGHT AT POST GRADUATE LEVEL

  M.Tech (Micro Electronics) Regular (July 2011-till date)
  - Analog CMOS Design
  - VLSI Design
  - Hardware Description Language (VHDL),
  - Designing with ASICS
  - Electronics System Design

  M.Tech VLSI Design (Modular)

  Analog VLSI Design, Low Power VLSI Design, CMOS RF Design,

  PG LABORATORIES DEVELOPED/PERFORMED
  - Microelectronics Lab
  - Microprocessor and Microcontroller Lab
  - VLSI Lab

PARTICIPATION IN TECHNICAL UNIVERSITY ACTIVITIES:

- **Dean, Undergraduate Studies and Entrepreneurship (UGSE)**, Dr. A.P.J. Abdul Kalam Technical University Uttar Pradesh, Lucknow, India. (Sept. 2019-**30th May 2022**).
- **Coordinator Uttar Pradesh Common Entrance Test (former UPSEE), UPCET 2021.** (Dec. 2020- June 2021).
- **Convener, Virtual Lab Cell,** Dr. A.P.J. Abdul Kalam Technical University Uttar Pradesh, Lucknow, India. (https://aktu.ac.in/pdf/Upload%20Virtual%20Lab%20Cell%20Report1.pdf)
- **Incharge Kalam- Centre for Innovation and Incubation of startup (K-CIIS)** https://innovation.aktu.ac.in/ **till July 2021.**
- **C**hief Guest for Inaugural Session of various Faculty Development Program (FDP) organized by affiliating Institute of AKTU.
- Member of Steering Committee for New Education Policy (NEP) system 2020.
- **Member of Standing Committee of Academic Autonomy for Private Institution of AKTU. (निजी संस्थानों के शैक्षिक स्वायत्ता हेतु प्राप्त आवेदनों पर विचार हेतु गठित स्टेन्डिंग कमेटी).**
- Member of AKTU MOOC Coordination Committee, affiliation committee, Autonomy Grant Committee, Internal Quality Assurance Cell (IQAC), Governing Council of Nalanda E-consortium, various Institute Inspection (Approval Process 2020-21) and Examination Committees (2019-2022)
- **Dean, Recourse Generation & Alumni Matter**. Dr. A.P.J. Abdul Kalam Technical University Uttar Pradesh, Lucknow, India, Sept. 2018- **Sept. 2019.**
- **Associate Dean, Undergraduate Studies and Entrepreneurshi**p, Dr. A. P. J. Abdul Kalam Technical University Uttar Pradesh, Lucknow, India (Jan. 2017-Sept. 2018)
- **External Member FRC/DRC Committee for PH.D Program, AMITY University, Lucknow (2016-till date)**
- **External Member DRC Committee for PH.D Program, MMMUT University Gorakhpur (2022-till date)**
- External Member of Board of Studies (BOS) EI Deptt IET, Barelliy University (2013-till Date)
- External Member of Board of Studies of IET, Dr. Ram Manohar Lohia Avadh Univ. Faizabad, Uttar Pradesh (2017 till date).
- **Joint Controller of Examination,** Dr. A. P. J. Abdul Kalam Technical University (**AKTU**), Lucknow, Uttar Pradesh, (Feb. 2015 to Jan. 2016)
- **Dy. Coordinator, Uttar Pradesh State Entrance Examination, Lucknow (UPSEE-2014**)
- Member Admission Committee M.Tech & M.Pharma, UPTU, Lucknow (2013-2014).
- Member Seat Matrix Committee, UPSEE (2013- 2014), UPTU, Lucknow
- Member of Scanning supervision Committee for UPSEE-2013
- Center Controller, State Entrance Examination (UPSEE) (2002-2008)
- Nodal Officer (Confidential), State Entrance Examination (2009-2013)

- Observer for UPSEE-2011 Counseling at Document Verification Choice Locking Center
- Member inspection committees regarding affiliation of Private Engineering Colleges (2002-2008)
- Convener of Hospitality Committee, Annual Convocation, UPTU (2005-2008).
- Member of Hospitality Committee, Annual Convocation, UPTU (2011-2014).
- Head/Dy. Examiner, Central Evaluation (UPTU) regarding Examinations (2002-2014)
- Papers Setter/Thesis Examiner for UG/PG Course of Various Universities Examination (2005-till date)
- Member Moderation Committee, Examination for Various Universities.
- Member flying Squad, End Semester Examination, UPTU (2004-2008).
- Group Leader (Flying Squad) Special Carry Over End Semester Exam UPTU (Sep 2011)
- External Member of Board of Studies (BOS) EI Deptt IET, Barelliy University (2013-2017)
- Member of Selection Committee, Allahabad University, Prayagraj UP.

Membership for Professional Societies:-

1. Life time member of **Institute of Engineers IE (M133861-1**)

   Project Guide for PG/UG/AMI Student registered from The Institute of Engineers (India), IE Kolkata (2011-till Date).

- Ashish Ranjan Srivastava (PG 41110022), "Wireless Transmitter." The Institute of Engineers (India), IE Kolkata, (2012)
- Mukesh Kumar Sahu (ST No. 509433-5), "Microcontroller based Eye Blink Sensor and Accident Prevention Device." The Institute of Engineers (India),IE Kolkata, (2013)
- Divya Khanuja (ST No. 561209-3), "Electronics Eye Controlled Security System." The Institute of Engineers (India),IE Kolkata, (2014)
- Anamika Shukla (ST No. 559386-2), "Microcontroller based Safety System for Train." The Institute of Engineers (India), IE Kolkata, (2014)
- Gopi Kant (ST No. 120177-3), "Working of 1000KW AM Medium Wave DRM (Digital Radio Mondiale) Super Power Transmitter." The Institute of Engineers (India), IE Kolkata, (2015).
- Neetu Vishwakarma (ST No. 613298-2), "Solar Tracking System & Its Application." The Institute of Engineers (India), IE Kolkata, (2016).

2. Life time member of **Institute of Electrical and Telecommunication Engineering IETE (M189081**)
3. Member Executive Committee, The Institute of Electronics Telecommunication Engineers (IETE), Lucknow Chapter, (2015-till date)
4. Life time member of **Indian Society for Technical Education** (**LM33784**)

Research & Publications

M.E. THESIS GUIDED (39)/M.TECH. THESIS UNDER GUIDANCE (3)

1. Garima Singh, "Performance Analysis of High Speed Low Voltage CMOS Full Adder Circuits, AIM&ACT, Banasthali University, Rajasthan , India.(2011-2012)
2. Divya Tripathi, "Design Analysis of High-Speed XOR/XNOR based Logic Circuits Suitable for Low Power VLSI Applications." Jayoti Vidyapeeth Women's University, Jaipur, Rajasthan, India (2012).
3. Meenakshi Shree, "Design Analysis of Mixed Chain Full Adder Circuits for VLSI Applications," JayotiVidyapeeth Women's University, Jaipur, Rajasthan, India (2012).
4. Ritika Mishra, "Low Power Design Analysis of Array Multipliers Using Hybrid CMOS Full Adder Circuits." Jayoti Vidyapeeth Women's University, Jaipur, Rajasthan, India (2012).
5. Sapna Dixit, "Performance Analysis of High Speed Adiabatic Digital Logic Circuits, AIM&ACT, Banasthali University, Rajasthan , India. (2012-2013)
6. Nidhi Gupta, "Realization and study of Low Power CMOS Current Feedback Amplifier"AIM &ACT, Banasthali University, Rajasthan, India. (2013-2014).
7. Rahul Verma, "Design and Analysis of Ultra Low Power SRAM Cache Memory Cell", IET, Lucknow, India (2014).
8. Monika Jain, "Study and Design of Low Power and Leakage Proof Digital Circuits", IET, Lucknow, India (2014).
9. Anjali Tiwari, Design and Performance Analysis of Dynamic Circuits using Multi-Threshold Technique for Low Power VLSI Circuits, AIM &ACT, Banasthali University, Rajasthan, India. (2015).
10. Vijata, "Design and Analysis of Different Topologies of CMOS Operational Trans-conductance Amplifier", IET, Lucknow, India (2015).
11. Archita Srivastava, "Implementation and Analysis of Adiabatic Logic Circuits." Jayoti Vidyapeeth Women's University, Jaipur, Rajasthan, India. (2015)
12. Shivani Shukla, "Temperature Dependent Leakage Power Characteristic of Dynamic Circuit in Nanometer CMOS Technologies." Jayoti Vidyapeeth Women's University, Jaipur, Rajasthan, India (2015).
13. Shweta Kumari, "Performance Analysis of GDI Based 1-Bit Full Adder Circuit for Low Power & High Speed Application." Jayoti Vidyapeeth Women's University, Jaipur, Rajasthan, India (2015).
14. Ravi Prakash Verma, "Filter Antenna Module using Substrate Integrated Waveguide." SRMS Barailly, Uttar Pradesh Technical University, Lucknow, India (2015).
15. Ankita Agarawal,"Cross layer optimization of Optical node in High Speed Network." M.Tech Modular, Uttar Pradesh Technical University, Lucknow, India (2015).
16. Shashank Gupta, "Design Of Hybrid Code Converters Using Modified Gate Diffusion Input Technique", IET, Lucknow, India (2016).
17. Shraddha Pandey, "Designing An Efficient JK and T Flip Flop using QCA with Power Dissipation Analysis.", IET, Lucknow, India (2016).
18. Sonali Singh, "Modular Design Of 2n*1 QCA Multiplexers and its application Via Clock Zone based Crossover", IET, Lucknow, India (2016)
19. Ragni Tripathi, "Design analysis of low pass GDI circuit in sub-threshold region", IET, Lucknow, India (2017)

20. Ritesh Singh, “Implementation of non-restoring reversible divider using QCA”, IET, Lucknow, India (2017)
21. Sakshi Gupta, “Efficient design of even and odd parity generator using different XOR-XNOR modules in VLSI design circuit”, IET, Lucknow, India (2017)
22. Yogesh Singh, “Design Analysis for SRAM in Nano-Technology”, M.Tech (Modular), AKTU, Lucknow, India (2018)
23. Shahneela Jamal Kidwai, “Design of Full Adder with self checking capability using QCA”, IET, Lucknow, India (2018)
24. Bhoopendra Vikram Singh, “Contact thickness effect on mobility of Organic Thin Film Transistor and tradeoff with current on-off ratio”, IET, Lucknow, India (2018)
25. Anshu Arya, “Performance Evaluation and design implementation of SRAM Semiconductor Memory in Nanotechnology”, IET, Lucknow, India (2018)
26. Abhishek Shukla, “Designing and Performance Comparison of Parity Generators Using Various XOR-XNOR modules”, IET, Lucknow, India, (2019).
27. Prashasti, “Performance Evaluation of High Speed and Low Power Digital Circuits using Energy Efficient XOR & XNOR logic gates”, IET, Lucknow, India, (2019).
28. Shivangi Jaiswal, “Performance Evaluation of Combinational circuit based on Energy Efficient Adiabatic Logic Technology for Ultra Low-Power Applications”, IET, Lucknow, India, (2019).
29. Sana, “Implementation of MGDI and Transmission Gate Based Hybrid CMOS Full Adders Using Triplet Design Approach”, IET, Lucknow, India, (2020)**.**
30. Priti Tripathi, “Low Power Shift Registers Using Contention Free Single-Phase Clocked Flip Flop”, IET, Lucknow, India, (2020).
31. Sweta Tripathi, “Low Power Voltage Controlled Oscillator (VCO) Using Multi-Threshold Transistors and Power gating Technique in Deep Submicron Technology”, IET, Lucknow, India, (2020).
32. Vivek Mishra, “Implementation of Process Supply Voltage and Temperature Compensated Supply Circuit for A Ring Oscillator”, IET, Lucknow, India, (2020).
33. Semba Walli, “MOSFET Based Memristor Emulator Circuit Analysis and Applications”, IET, Lucknow, India, (2020).
34. Vivek Saxena, “Power Efficient Frequency Divider Circuit for VLSI Applications”, IET, Lucknow, India, (2020).
35. Aishita Verma, “Performance Analysis of Hybrid Full Adder and Multiplier Topologies for Fast Computation”, IET, Lucknow, India, (2021).
36. Nivedita Rai, “Performance Evaluation and Analysis of Comparator Design in VLSI Application”, IET, Lucknow, India, (2021).
37. Ayushi Kirti Singh, “Design and Realization of QCA based Logic Designs”, IET, Lucknow, India, (2022).
38. Kunal Kumar, “Design of Low Power Dynamic Comparator Topologies for VLSI”, IET, Lucknow, India, (2022).
39. Mohd Saqib, “Performance Evaluation and Analysis of Differential Dual stage delay cells VCO”, IET, Lucknow, India, (2022).

PH.D. THESIS AWARDED (02):

1. Neeraj Kumar Mishra PhD/13/ECE/1134, "Design and Testability of Diverse Reversible Logic Circuits for Low cost Nanoelectronics Application," Dr. A.P.J. Abdul Kalam Technical University Uttar Pradesh (AKTU), Lucknow, India **Awarded (2017)**
2. Divya Tripathi, (PHD/13/ECE/1413, "Performance Evaluation of Low Power, High Speed Arithmetic Logic Circuits in Nanotechnology," Dr. A.P.J. Abdul Kalam Technical University Uttar Pradesh (AKTU), Lucknow, India **Awarded (2022)**

PH.D. THESIS WRITING IN PROCESS (02):

1. **Shilpi Gupta**, (PHD/16/ECE/2175), "**Design and Analysis of Tunnel Field Effect Transistor for Low Power Application**," Dr. A.P.J. Abdul Kalam Technical University Uttar Pradesh (AKTU), Lucknow, India. (Thesis submission in process)
2. **Jyoti Garg, (PHD/15ECE/2055),** "**Performance Evaluation of Non Volatile Memory for Low Voltage VLSI Application**", Dr. A.P.J. Abdul Kalam Technical University Uttar Pradesh (AKTU), Lucknow, India. (Thesis writing in process).

PH.D. THESIS UNDER GUIDANCE (07):

1. Digvijay Pandey, PHD/16ECE2172
2. Anum Khan PHD/17/ECE/2211
3. Aunrag Yadav PHD/19/ECE/2461
4. Sana PHD/20/ECE/2610
5. Priyanka Shakya PHD/21/ECE/2657
6. Preeti Pripathi PHD/21/ECE/2713
7. Sheetal Singh PHD/21/ECE/2715

Enrollment No	Full Name	Mobile No	Email Id	Topic
16ECE2172	Digvijay Pandey	8318637549	digit11011989@gmail.com	Performance Analysis on Text Extraction from Complex Degraded Images
17ECE2211	Anum Khan	7800318000	anumkhan0902@gmail.com	Performance Evaluation of High Speed Digital Circuits In Nanotechnology Architecture
19ECE2461	Anurag Yadav	7651921092	anuraglko2014@gmail.com	Performance Evaluation of Low Power High Speed VLSI Design and Application with Data Converter Architecture
20ECE2610	SANA	9140159469	sana.aliya1403@gmail.com	Low Power VLSI Circuits
21ECE2657	Priyanka Shakya	9451452839	shakyapriyanka89@gmail.com	Design and Analysis of STT-RAM using Logic in Memory Architecture
21ECE2713	Preeti Tripathi	7379734552	1805267006@ietlucknow.ac.in	A Novel Cross-Latch Shift Register Scheme for Low Power Applications
21ECE2715	Sheetal Singh	7006425805	ssingh.ecd.cf@ietlucknow.ac.in	High frequency circuit design in microelectronics

PUBLICATIONS: *https://scholar.google.co.in/citations?user=d-g3yEoAAAAJ&hl=en*

PUBLICATIONS: JOURNAL

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1. Jyoti Garg, Subodh Wairya, "*Design of Low Power Arithmetic logic unit using SHE assisted STT / MTJ* Int. J. Com. Dig. Sys., ISSN (2210-142X). (Accepted)
2. Aishita Verma, Anum Khan, Subodh Wairya, "*Design and Analysis of Efficient Vedic Multiplier for Fast Computing Applications*", Int. J. Com. Dig. Sys., ISSN (2210-142X).
3. Digvijay Pandey, Subodh Wairya, *"An optimization of target classification tracking and mathematical modelling for control of autopilot*", The Imaging Science Journal, Taylor & Francis, pp. 1-16, 2023. DOI: https://doi.org/ 10.1080/13682199. 2023.2169987.
4. Mohd Saqib, Subodh Wairya and Anurag Yadav, "A 6.7GHz, 89.33 µW power and 81.26% tuning range dual input ring VCO with PMOS varactor", Journal of Circuits, Systems, and Computers (JCSC) ISSN: 0218-1266. https://doi.org/10.1142/S0218126623501992

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5. Jyoti Garg, Subodh Wairya, "Performance Evaluation of Low Power Hybrid Combinational Circuits using Memristor", International Journal of Electrical and Electronics Research (IJEER), 2022, 10(4), pp. 988–993 ISSN: 2347-470X (Online) FOREX Publication 10.37391/IJEER.
6. Vivek Mishra, Anurag Yadav, Subodh Wairya, "Design of Compensated Supply Circuit Topology for a Ring Oscillator", International Journal of Computing and Digital System, July-2022. Int. J. Com. Dig. Sys. 12, No.1 (Jul-2022) pp. 1005-1017. https://dx.doi.org/10.12785/ijcds/120181. ISSN (2210-142X).
7. D Pandey, S Wairya, B Pradhan, Wangma, "Understanding COVID-19 response by twitter users: A text analysis approach", pp. 1-6, Heliyon (8), 2022
8. Divya Tripathi, Subodh Wairya, A Cost Efficient QCA Code Converters for Nano Communication Applications, International Journal of Computing and Digital System, July-2022. Int. J. Com. Dig. Sys. 12, No.1 (Jul-2022), pp. 345-352. https://dx.doi.org/10.12785/ijcds/120128. ISSN (2210-142X).
9. Anum Khan, Subodh Wairya, "Performance Evaluation of Highly Efficient XOR and XOR-XNOR Topologies using CNTFET for Nanocomputation", International Journal of Computing and Digital System, July-2022. Int. J. Com. Dig. Sys. 12, No.1 (Jul-2022) pp. 225-236. https://dx.doi.org/10.12785/ijcds/120120. ISSN (2210-142X).
10. Pandey, D., Wairya, S., Sharma, M. et al "An approach for object tracking, categorization, and autopilot guidance for passive homing missiles", Aerospace Systems (2022): pp. 1-14. Springer Nature Singapore, https://doi.org /10.1007/s42401-022-00150-0.
11. Shilpi Gupta, Subodh Wairya, Shaliendra Singh, "S. Design and Analysis of Triple Metal Vertical TFET Gate Stacked with N-Type SiGe Delta-Doped Layer", Volume-14 Issue-8, June 2022, pp. 4217-4225 Silicon 2022. https://doi.org/10.1007/s12633-021-01211-3
12. Anurag Yadav, Subodh Wairya, "Performance and Area Optimization of SRAM Cell in Nanotechnology Application", International Journal of Computing and Digital Systems (Int. J. Com. Dig. Sys. 11, No.1 (Mar-2022)) pp. 1009-1026, March 2022, https://dx.doi.org/10.12785/ijcds/110182, ISSN (2210-142X).
13. Binay Kumar Pandey, Digvijay Pandey, Subodh Wariya, et al "Application of Integrated Steganography and Image Compressing Techniques for Confidential Information Transmission, Cyber Security and Network Security, 169-191, Publisher John Wiley & Sons, Inc.
14. Pandey, Digvijay and Subodh Wairya. "A Novel Algorithm to Detect and Transmit Human-Directed Signboard Image Text to Vehicle Using 5G-Enabled Wireless Networks." IJDAI vol.14, no.1 2022: pp.1-11. http://doi. org/10.4018/IJDAI.291084.

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15. Akhil Gupta, Rohit Anand, Digvijay Pandey, Nidhi Sindhwani, Subodh Wairya, Binay Kumar Pandey , Manvinder Sharma: "Prediction of Breast Cancer Using Extremely Randomized Clustering Forests (ERCF) Technique: Prediction of Breast Cancer." IJDST vol.12, no.4 2021: pp.1-15. http://doi.org/10.4018/IJDST.287859

16. Pandey, B. K., Pandey, D., Wairya, S., & Agarwal, G. (2021). Deep Learning and Particle Swarm Optimisation-Based Techniques for Visually Impaired Humans' Text Recognition and Identification. Augment Hum Res 6, 14 (2021). https://doi.org/10.1007/s41133-021-00051-5.

17. Pandey, B. K., Pandey, D., Wairya, S., & Agarwal, G. (2021). An Advanced Morphological Component Analysis, Steganography, and Deep Learning-Based System to Transmit Secure Textual Data. International Journal of Distributed Artificial Intelligence (IJDAI), 13(2), 40-62. http://doi.org/10.4018/IJDAI.2021070104.

18. Pandey, Binay Kumar and Pandey, Digvijay and Wariya, Subodh and Agarwal, Gaurav (2021) A Deep Neural Network-Based Approach for Extracting Textual Images from Deteriorate Images. EAI Endorsed Transactions on Industrial Networks and Intelligent Systems, 8 (28). e3. ISSN 2410-0218.

19. Digvijay Pandey, , Subodh Wairya , Raghda Salam Al.Mahdawi , Saif Al-din M. Najim , Haitham Abbas Khalaf , Shokhan M. Al-Barzinji , Ahmed J. ObaideInt. "Secret data transmission using advance steganography and image compression", International Journal of Nonlinear Analysis and Applications. Volume 12, Special Issue, Winter and Spring 2021, 1243-1257 ISSN: 2008-6822 (electronic) http://dx.doi.org/10.22075/ijnaa.2021.5635

20. Shilpi Gupta, Subodh Wairya, Shaliendra Singh, "Analytical modeling and simulation of a triple metal vertical TFET with hetero-junction gate stack", Superlattices and Microstructures Journal. Volume 157, September 2021, 106992. https://doi.org/10.1016/j.spmi.2021.106992.

21. Divya Tripathi, Subodh Wairya, "An Energy Dissipation and Cell Optimization of Vedic Multiplier Topologies for Nanocomputing Applications", Turkish Journal of Computer and Mathematics Education, Vol.12 No.14 (2021), 1490– 1510. https://turcomat.org/index.php/turkbilmat/article/view/10473/7889

22. Digvijay Pandey, Shaji George, Bashiru Aremu, Subodh Wariya, Binay Kumar Pandey, "Critical Review on Integration of Encryption, Steganography, IOT and Artificial Intelligence for the Secure Transmission of Stego Images", Scientific Research Journal of Engineering and Computer Science, 2021, Sci Res Jr Eng Comp Sci.1(1):-33-36, Volume 1,Issue: 1 (June-July) ISSN On line: 2788- 9408. DOI: 10.47310/srjecs.2021.v01i01.005.

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23. Sana, Anum Khan, Subodh Wairya, "Design and Analysis of Hybrid full adder Topology using Regular and Triplet Logic Design", International Journal of Innovative Technology and Exploring Engineering (IJITEE) (Blue Eyes Intelligence Engineering & Sciences Publication), Volume-9 Issue-12, October 2020 pp. 348-354, DOI: 10.35940/ijitee.L8024.1091220, Oct. 2020. ISSN: 2278-3075, http://www.ijitee.org/wp-content/uploads/papers/v9i12/L80241091220.pdf.

24. Pandey, D., Pandey, B.K. & Wairya, S. "Hybrid deep neural network with adaptive galactic swarm optimization for text extraction from scene images", Soft Comput. 25(2): 1563-1580 (2021), Electronic ISSN: 1433-7479, Print ISSN: 1432-7643, https://doi.org/10.1007/s00500-020-05245-4*, Indexed in Thomson Reuters, (SCIE impact factor 3.05)*

25. Divya Tripathi, Subodh Wairya. "Energy Efficient Code Converter For Nanotechnology Applications", Journal of Critical Reviews (JCR). 2020; 7(13): 2916-2925, ISSN 2394-5125, doi:10.31838 /jcr.07.13.448

26. Digvijay Pandey, Binay Kumar Pandey, Dr. Subodh Wairya, Dr. Randy Joy M. Ventayen, Bilal Khan, Dr Monika Gupta, Dr. Tribhuwan Kumar. "Analysis of Text Detection, Extraction and Recognition from Complex Degraded Images and Videos" Journal of Critical Reviews (JCR) 2020; Vol. 7, Issue 18, pp. 427-433, ISSN 2394-5125, doi: 10.31838/jcr.07.18.63,

27. D. Pandey, Binay Kumar Pandey, and Subodh Wairya, "An Approach To Text Extraction From Complex Degraded Scene", International Journal of Computational and Biological Sciences (IJCBS), Vol. 1 No. 2 (2020): ISSN 2708-3551 (Online).

28. Tripathi Divya & Subodh Wairya "An Energy Efficient Binary Magnitude Comparator for Nanotechnology Application", International Journal of Recent Technology and Engineering (Blue Eyes Intelligence Engineering & Sciences Publication) Vol.8 Issue-6, pp. 430-436, DOI:10.35940/ijrte.F7000.038620 March 2020. ISSN: 2277-3878, https://www.ijrte.org/wp-content/uploads/papers/v8i6/F7000038620.pdf.

2019

29. Abhishek Shukla, Subodh Wairya, "Design of Odd-Even Parity Generator using Six Transistors XOR-XNOR Module", International Research Journal of Engineering and Technology (IRJET), Vol. 6 Issue 11, Nov 2019, e-ISSN: 2395-0056.

30. **Digvijay Pandey**, Binay Kumar Pandey, Subodh Wairya "Study of Various Types Noise and Text Extraction Algorithms for Degraded Complex Image" **Journal of Emerging Technologies and Innovative Research**, vol. 6, Issue 6. pp. 234-246, June 2019. ISSN: 2349-5162. *UGC Approved Journal.*

31. **Digvijay Pandey**., Binay Kumar Pandey, Subodh Wairya "Study of Various Techniques Used for Video Retrieval" **Journal of Emerging Technologies and Innovative Research**, vol. 6, issue 6, pp.850-853, June 2019. ISSN Number: 2349-5162.

32. Prashasti, Shivangi Jaiswal, Anum Khan and Subodh Wairya, "High Performance and Low Power D Flip-Flop using Pulsed Latch Technique" **International Journal of Applied Engineering Research (IJAER)**, ISSN 0973-4562, Vol. 14, no.2 (Special Issue), pp. 301-305 (2019) ijaerv14n2spl_53.

33. Singh, Amrita, Manoj Kumar Jain, and Subodh Wairya. "Novel Lossless Grounded and Floating Inductance Simulators Employing a Grounded Capacitor Based in CC-CFA." **Journal of Circuits, Systems, and Computers**, Vol. 28, Issue 6, Page 1950093 (2019), ISSN: 0218-1266, ***Indexed in Thomson Reuters (SCIE impact factor =0.595),*** https://doi.org/10.1142/S0218126619500932.

34. S. Kidwai and Subodh Wairya, "Study of QCA based digital logic circuits to be used in nanotechnology", **Global Journal of Engineering Science and Researches,[COTII -2018]** pp. 289-295, July 2018, ISSN 2348–8034.

2018

35. Divya Tripathi and Subodh Wairya," Performance evaluation of low power Carry save adder for VLSI applications" International Journal of VLSI design & Communication Systems (VLSICS) Vol. 9, No.3, pp. 51-58, June 2018, DOI: 10.5121/vlsic.2018.9305, ISSN: 0976-1357. https://ssrn.com/abstract=3288356

36. Raj Vikram Singh, Subodh Waira, Rajiv Kumar Singh & Harsh Vikram Singh, "Robust Watermarking using Genetic Algorithm in DCT Domain", International Journal of Engineering and Technology (IJET), Vol. 7, No. 3(12). Special Issue 12, pp. 1202-1204, 2018. ISSN 1793-8236 (Online),. DOI: 10.14419/ijet.v7i3.12.17837.

2017

37. Neeraj Kumar Misra, Subodh Wairya, Bibhash Sen, "Design of conservative, reversible sequential logic for cost efficient emerging nano circuits with enhanced testability". ***Ain Shams Engineering Journal, Elsevier*** *(Amsterdam, Netherlands)*, vol. 9, issue 4, pp. 2027-2037, December 2018, DOI: 10.1016/j.asej.2017.02.005, ISSN: 2090-4479, **Indexed in Thomson Reuters (SCIE impact factor =3.091)**

38. Neeraj Kumar Misra, Bibhash Sen, Subodh Wairya, "Towards designing efficient reversible binary code converters and a dual-rail checker for emerging nanocircuits". ***Journal of Computational Electronics, Springer (New York, USA)***, 17 pages, vol. 16, issue 2, pp. 442-458, Feb 25, 2017, DOI: 10.1007/s10825-017-0960-4, ISSN: 1569-8025, **Indexed in Thomson Reuters (SCIE impact factor =1.63)**

39. Neeraj Kumar Misra, Bibhash Sen, Subodh Wairya, Bandan Boi, "Testable Novel Parity-Preserving Reversible Gate and Low-Cost Quantum Decoder Design in 1D Molecular-QCA". ***Journal of Circuits, Systems, and Computers, World Scientific*** *(Singapore)*, 26 pages, vol. 26, issue 09, pp. 1-26, 28-Feb-2017. DOI: 10.1142/S0218126617501456, ISSN: 0218-1266. ***Indexed in Thomson Reuters (SCIE impact factor =0.595)***

40. Neeraj Kumar Misra, Bibhash Sen, Subodh Wairya, "Novel Tree Structure Based Conservative, Reversible BCD Adder With Added Testability In Quantum Circuits", ***Journal of Computational and Theoretical Nanoscience*** (Valencia, California, USA)**,** vol. 14(5), pp. 1-13, 1-May-2017, ISSN: 1546-1955, DOI:10.1166/jctn.2017.6772, ***Indexed in SCOPUS***

41. Neeraj Kumar Misra, Bibhash Sen, Subodh Wairya, "Novel Conservative Reversible Error Control Circuits Based On Molecular-QCA"**,** ***International Journal of Computer Applications in Technology, Inderscience Publishers*** (Switzerland**)**, vol. 56, no. 1, 13-September-2017, DOI: 10.1504/ IJCAT. 2017.086558, ISSN**:** 1741-5047.**Indexed in Thomson Reuters (ESCI)**

42. Neeraj Kumar Misra, Bibhash Sen, Subodh Wairya, "Designing of an Energy-Efficient Nanoelectronics Architecture for Binary Comparator Based On Quantum-Dot Cellular Automata", ***SHRISTI : A Journal of Energy, Environment & Ecology at School of Management Science, Lucknow*** **at School of Management Science, Lucknow,** 2017.

2016

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Dr. Subodh Wairya is a Professor of Electronics and Communication Engineering Department (NBA Accredited) at Institute of Engineering & Technology, (I.E.T) Lucknow, Uttar Pradesh, India. He received Doctoral degree from Motilal Nehru National Institute of Technology (MNNIT) Allahabad, India, Master of Engineering (Telecommunication) degree from Jadavpur University, Kolkata and the B.Tech (Electronics Engineering) degree from H.B.T.I., Kanpur, India. He has more than **Twenty Nine years experience in teaching and research**. He has served as Scientist "B" in Defense Research & Development Organization (DRDO) and Graduate Engineer (Design Project) in Hindustan Aeronautical Limited (HAL), Lucknow from 1994 to 1996. He has published more than **150 National and International papers** in various Journal and Conference of repute. Dr. Subodh Wairya is actively associated with membership for Professional Societies as Life time member of Institute of Engineers (IE), Institute of Electrical and Telecommunication Engineering (IETE) and Indian Society for Technical Education (ISTE).He also served as Dean, Undergraduate Studies and Entrepreneurship (UGSE) & Convener Virtual Lab Cell for Dr. A.P.J. Abdul Kalam Technical University, Uttar Pradesh, Lucknow. His Current research interests are High Speed Network, Image Processing, VLSI Circuits and Analog System design& Image Processing.