

RANITESH GUPTA



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Professional Summary:

- Pursuing Ph.D. in Electrical Engineering from Dr. APJ A.K.T.U., Lucknow.
- M.Tech in Power Electronics and Drives in Electrical Department from National Institute of Technology, Kurukshetra.
- B.Tech in Electrical Engineering from Azad Institute of Engineering and Technology, Lucknow affiliated to Uttar Pradesh Technical University, Lucknow.
- Gate-2007 Qualified.

Academic Qualification:

- 12th from Lucknow Public Inter College, Lucknow (U.P. Board) in 2001.
- 10th from J.P.S. Inter College, Lucknow (U.P. Board) in 1999.

Experience: (Total: 11Years)

- Currently working as Assistant Professor (Contractual) in Electrical Engineering Department, Institute of Engineering and Technology, Lucknow from September 2021.
- Three years teaching experience as Guest Lecturer (Electrical Engineering Department) in D.S.M.N.R.University, Lucknow from July 2018 to August 2021.
- 11 months teaching experience as Guest Lecturer (Electrical Engineering Department) in Institute of Engineering and Technology, Lucknow from July 2017 to June 2018.
- Six year teaching experience as Assistant Professor (Electrical Engineering Department) in Sagar Institute of Technology and Management, Barabanki, Uttar Pradesh from July 2010 to July 2016.
- One year teaching experience as a Lecturer (Electrical Engineering Department) in National Institute of Technology, Kurukshetra, Haryana from July 2009 to June 2010.

Training:

- Six Week summer training on “Neutral Section” from Research Development and Standard Organization (R.D.S.O.), Lucknow.

Major Projects:

- P.G. level: “Modeling and Simulation of Three-Phase Inverter.”
- U.G. level: “Fabrication of Thyristorized Linear Phase Controller for Open Loop Drives.”

International Research Publications:

- Ranitesh Gupta, “**Comparative Study of Total Harmonic Distortion for Three Phase Voltage Source Inverter**”, in International Journal of Engineering Studies, Volume 2, Number 2 (2010), pp. 175–182, 2010.
- Nikhil Raj, **Ranitesh Gupta**, Vikram Chopra, “**A Low Voltage high performance OTA in 0.18 micron with High Linearity**”, in First International Workshop on VLSI, proceedings of Springer (LNCS), pp.712-719, 2010.
- Nikhil Raj, **Ranitesh Gupta**, Vikram Chopra, “**Bulk driven OTA in 0.18 micron with High Linearity**”, in 3rd IEEE International Conference on Computer Science and Information Technology (IEEE ICCSIT 2010),pp.478-482, 2010.
- **Ranitesh Gupta**, Sarika Shrivastava, C.S. Azad and V.K. Srivastava, “**Improvement in Design and Erection Methodology of Transmission Towers**”, in International Journal of Electronic and Electrical Engineering, Volume 3, Number 1(2010), pp. 205-210, 2010.
- Aditya Kr. Rawat, Ashutosh kr. Singh, Dinesh Nishad, **Ranitesh Gupta**, “**Optimal Dispatch of Power System with Integrated System Constraints**”, International Journal of Engineering Research & Technology (IJERT), Volume 2, Issue 03, pp. 500-505, 2021.

Computer Proficiency:

- **Soft Skill:** Basics of C, C++.
- **Technical Skill:** Matlab, PSIM, Typhoon Hill.

Personal Details:

Father’s Name : Mr. Ramesh Chandra Gupta
Date of Birth : July 05, 1985
Marital Status : Married
Address : 363/76, Naveen Nagar, Hasan Ganj Bawli, Lucknow, U.P. (226017)
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I hereby declare that all the information provided by me in this resume is true and best to my knowledge and belief.

Date:

Place: Lucknow.

(RANITESH GUPTA)